

Sir:

PATENT Customer No. 22,852 Attorney Docket No. 04329.3138

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	
Kouji MATSUO	) Group Art Unit: 2814
Application No.: 10/660,559	) Examiner: Weiss, Howard
Filed: September 12, 2003	Confirmation No.: 6404
For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME	
Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	

## RESPONSE TO RESTRICTION REQUIREMENT

In the Office Action mailed October 12, 2004, with a period for response extending through November 12, 2004, the Examiner required restriction under 35 U.S.C. § 121 between the following groups of claims:

Group I - Claims 1-11, characterized by the Examiner as drawn to a semiconductor device; and

Group II - Claims 12-19, characterized by the Examiner as drawn to a process for making a semiconductor device.

Applicant provisionally elects to prosecute the group characterized by the Examiner as Group I, claims 1-11, without traverse.

Please grant any extensions of time required to enter this response and charge any additional required fees to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

Dated: November 9, 2004

Raieev G

Reg. No. 55,873